

ABSTRACT OF THE DISCLOSURE

A video data transfer system which increases the rate of capturing video data into a system memory without being affected by a data bus and which prevents the display of data on a display unit from being affected even when data is being captured. Video data from a video processor 21 is sent, not via a frame buffer 14 but via a capturing-only path 26, directly to a system bus 17. This means that video data may be captured into a system memory 18 regardless of the status of a real time output path 25. On the other hand, video data is sent to a display 16 via the real time output path 25 provided independently of the capturing-only path 26. This means that video data may be sent to the display 16 at a constant rate regardless of whether or not data is being captured.